

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,894,467 B2  
DATED : May 17, 2005  
INVENTOR(S) : Alexandre Pons et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6.

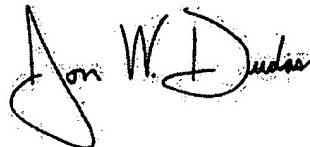
Line 51, should read -- forms terminal 12 and receives current voltage Vout across --.  
Line 66, should read -- output terminal O1 the control signal of gate G1. Second --.

Column 7.

Line 19, should read -- minals 13 and 14 of application of voltages V2 and Vmid. --.

Signed and Sealed this

Twenty-seventh Day of September, 2005



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*